

Features

- LDO general purpose voltage regulator
- External power device
- Digital buffer function
- Low IQ: 100 μ A
- High output current: 1A
- Large input voltage range, from 1.4 V to 3.6 V
- Fast transient response
- Soft start
- Small output capacitor: 10 μ F

General Description

LDO: The SGC79100 is a low-dropout, high current, fast response, low quiescent linear regulator IP solution with external NPN (or NMOS) power device for integration into SoC. It offers high power rejection (PSRR) and soft start. The IP uses advanced control techniques to achieve excellent transient response and still maintain low dropout and low quiescent. The SGC79100 is stable with a 10 μ F output capacitor. It is specified from $T_J = -40\text{ }^\circ\text{C}$ to $+125\text{ }^\circ\text{C}$ and is designed to achieve 4 % overall accuracy (over Load/Line/Temp)

Applications

Quick Reference
