

COMMITMENT

Features

- · Advanced Power Controller w/ POR
- Large input voltage range: 1.6 1.98 V
- Low I_Q: 45 μA
- Power-On-Reset
- External voltage detector w/ UVLO
- · Reference voltage with 4 decoupled outputs
- Bias current with 4 outputs
- 3-phase 1.2 MHz clock generator
- · Level shifter disable signal generator
- Power-up sequencing controller

General **Description**

The SGC21412 is a versatile and scalable Power Controller for SoC integration. When used standalone acts as a robust POR with monitoring functions. When part of a PMU (Power Management Unit) acts as the backbone. On a PMU it provides all the essential support functions, such as high PSRR voltage and current references, Under Voltge Lock Out (UVLO), start-up sequencing, Power On Reset (POR) and Power On Controller (POC). It can monitor multiple regulated voltages and generate the reset signals accordingly. The internal references have digital trimming bits for finetuning the performance or yield. The SGC21412 does not require any external decoupling capacitor or pin and achieves better than 1 % voltage reference accuracy over temperature. It operates from $T_i = -40$ °C to +125 °C.

Applications

- Standalone POR & Vref
- Standalone Voltage Monitor
- Backbone for PMU/PMIC

Application Diagram

Quick Reference

SYMBOL	DESCRIPTION	MIN	TYP	MAX	UNIT
V _{IN}	Input Voltage	1.60	-	1.98	V
V _{DVDD}	Digital Voltage	0.99	1.20	1.54	V
V _{ref}	Ref. Voltage	-	0.750	-	V
I _{bias}	Ref. Current	-	1	-	μΑ
F _{CLK}	Ref. Frequency	-	1.2	-	MHz

C) SGC21412	- Ivi	dydd	
g[2:0] vg[4:0]	VREE	()	vreft_ vreft
#[2:0]	IBIAS	(w)	ibp1u0q ibp1u0c
visos	BOR	lock	
	POR	<u> </u>	P952
vanst BEF OK	M.CN (optional)		vanatok vananok
en	OSC (optional)	(w)	clktphs clktphs clksphx
# refgn	d †agnd	† dgnd	