

Features

- LDO general purpose Low-Dropout regulator
- Low IQ: 20 μ A
- Large input range, from 2.3 V to 5.5 V
- Maximum output current: 150 mA
- Available in multiple output versions: fixed or programmable from 1.0 V to 4.6 V
- Maximum output current: 400 mA
- Best in class transient: 40 mV
- Soft start and overcurrent protection
- Core area : 0.09 mm²

Applications

Applications Diagram

General Description

LDO: The SGC73150 is the 150 mA regulator from a family of low-dropout (LDO), high current, ultra fast response, low quiescent linear regulator IP solution for integration on SoC. It offers high power rejection (PSRR), soft start and fold-back over current protection. The IP uses advanced control techniques to achieve excellent transient response and still maintain low dropout and low quiescent. The SGC73150 is stable with a 1.0-3.3 μ F ceramic output capacitor. It is specified from $T_J = -40\text{ }^\circ\text{C}$ to $+125\text{ }^\circ\text{C}$ and is designed to achieve 3 % overall accuracy (over Load/Line/Temp).

Quick Reference
