

Features

- LDO capless Low-Dropout regulator
- Low voltage input range, from 3.0 V to 3.6 V
- Fast transient response
- Soft start and over current protection

General Description

LDO: The SGC75 is a low-dropout, high current, fast response, low quiescent linear regulator IP solution for integration on SoC. It offers high power supply rejection (PSR), soft start and over current protection. The IP uses advanced control techniques to achieve excellent transient response and still maintain low dropout and low quiescent. The SGC75 family is stable without the need for any external output capacitor. It is specified from $T_J = -40\text{ }^\circ\text{C}$ to $+125\text{ }^\circ\text{C}$ and is designed to achieve 4% overall accuracy (over Load/Line/Temp/Process)

Applications

Quick Reference

Applications Diagram