

Features

- Bandgap POR & CTRL Advanced Power Controller
- Low IQ: 25 μ A
- Large input voltage range
- Internal supply voltages generation
- Level shifter disable signal generation
- Vref generation with independently decoupled outputs
- Independent bias current outputs
- Power-up sequencing controller, External voltage detector and Power-On-Reset

Applications

Applications Diagram

General Description

POR Power on Reset: The SGC21412 is an Advanced Power Controller for integration into SoC. The SGC21412 is based on a common platform that can be scaled for each system needs. It includes all the necessary auxiliary functions to support the Power Management Unit (PMU). Additionally it also generates the reset signals based on the monitoring of the regulated voltages. It offers high power rejection (PSRR), start-up sequencing and independent voltage reference decoupling without the need for extra package pins. The generated references have digital trimming bits for increased performance or yield improvement. The SGC21410 is stable without any external decoupling capacitor. It is specified from $T_J = -40\text{ }^\circ\text{C}$ to $+125\text{ }^\circ\text{C}$ and is designed to achieve 1 % overall voltage reference accuracy (over Load/Line/Temp/Process)

Quick Reference
